

U.S. Patent Serial No.: 08/305,528

Atty. Docket No.: 29284/327

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

MAR 05 1998

GROUP 2100

APPLICANTS : WATANABE, T. et al.

SERIAL NO. : 08/305,528

FILED : September 13, 1994

FOR : SEMICONDUCTOR DEVICE INCORPORATING  
INTERNAL POWER SUPPLY FOR COMPENSATING FOR  
DEVIATION ON OPERATING CONDITION AND  
FABRICATION PROCESS CONDITIONS

GROUP ART UNIT : 2511

EXAMINER : A. Zarabian

ASSISTANT COMMISSIONER FOR  
PATENTS AND TRADEMARKS  
Washington, D.C. 20231

**CLAIM TO CONVENTION PRIORITY UNDER 35 U.S.C. § 119**

SIR:

Applicants claim the convention priority of the following Japanese Applications:

- (1) JP-60-161467 filed July 22, 1985
- (2) JP-61-17929 filed January 31, 1986
- (3) JP-61-30846 filed February 17, 1986
- (4) JP-61-286610 filed December 3, 1986
- (5) JP-62-35519 filed February 20, 1987
- (6) JP-62-168652 filed July 8, 1987

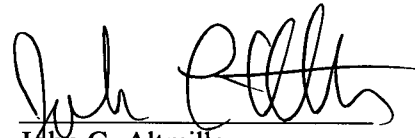
Priority to the first three of the applications was claimed in prior U.S. Application Serial No. 130,640 filed December 9, 1987 now issued as U.S. Patent 4,837,462 priority to the second three was claimed in Serial No. 126,485 filed November 30, 1987 now issued as U.S. Patent 4,873,673, the present application claims priority back to those two applications.

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(See the insertion made in the preliminary amendment of September 12, 1997.) Certified copies of the first three Applications were already submitted in Serial No. 130,640 and certified copies of the last three in Serial No. 126,485. Thus it is not believed that additional certified copies are necessary.

Respectfully submitted,

Date: 27 February 1998

  
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